

<b>Notice of References Cited</b>	Application/Control No. 10/749,020	Applicant(s)/Patent Under Reexamination CLEMENTI ET AL.	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2004/0203250	10-2004	Clementi et al.	438/727
	B	US-6,027,972	02-2000	Kerber, Martin	438/257
	C	US-5,510,286	04-1996	Kim, Jae K.	438/637
	D	US-5,310,693	05-1994	Hsue, Peter C. C.	438/262
	E	US-5,998,287	12-1999	Huang, Heng Sheng	438/587
	F	US-5,688,705	11-1997	Bergemont, Albert	438/588
	G	US-6,624,027	09-2003	Daemen et al.	438/264
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 182-83, 175, 191, 407-408.
	V	Streetman, Solid State Electronic Devices, 1990, Prentice Hall, 3rd ed., p. 332.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.